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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*	ATTORNEY DOCKET NO. **AUS920030489US1**	SERIAL NO.
	APPLICANT **DeWitt et al.**	
	FILING DATE	GROUP ART UNIT

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
LT		4,291,371	09/22/1981	Holtey	364/200	
		5,103,394	04/07/1992	Blasciak	395/575	
		5,212,794	05/18/1993	Pettis et al.	395/700	

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EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	NO
LT	JP	2000-29731A	12/08/1999	Japan			
	JP	2000-347863A	12/15/2000	Japan			

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LT		"Enable debuggers as an objective performance measurement tool for software development cost reduction", #444188, *Research Disclosure*, p. 686, April 2001.
		Kikuchi, "Parallelization Assist System", *Transactions of Information Processing Society of Japan*, v. 34, n. 9, pp. 1158-1169, 09/1993.
		"Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", *Cluster Computing*, v. 4, n. 3, pp. 189-196, 07/2001.
		Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Superscalar Architectures", *Proceedings of 2000 International Conference on Computer Design*, pp. 163-172, 09/2000.
		Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", *Transactions of Information Processing Society of Japan*, v. 36, n. 8, pp. 1995-2006, 08/1995.
		Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", 19th IEEE International Performance, Computing, and Communications Conference, pp. 516-524, 02/2000.

DATE CONSIDERED	06/08/2006	EXAMINER	/Loan Truong/

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LT		5,689,712	11/18/1997	Heisch	395/704	
		5,691,920	11/25/1997	Levine et al.	364/551.01	
		5,774,724	06/30/1998	Heisch	395/704	
		5,930,508	07/27/1999	Faraboschi et al.	395/706	
		5,937,437	08/10/1999	Roth et al.	711/202	
		5,938,778	08/17/1999	John, Jr. et al.	714/45	
		5,966,537	10/12/1999	Ravichandran	395/709	
		5,987,250	11/16/1999	Subrahmanyam	395/704	
		6,070,009	05/30/2000	Dean et al.	395/704	
		6,101,524	08/08/2000	Choi et al.	709/102	

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LT		***Intel Architecture Software Developer's Manual, Volume 3: System Programming*, Appendix A: Performance-Monitoring Events, pp. A1-A22, 01/1999.**

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LT		6,134,676	10/17/2000	VanHuben et al.	714/39	
	US	6,189,141 B1	02/13/2001	Benitez et al.	717/4	
	US	6,192,513 B1	02/20/2001	Subrahmanyam	717/5	
	US	6,206,584 B1	03/27/2001	Hastings	395/183.11	
	US	6,223,338 B1	04/24/2001	Smolders	717/4	
	US	6,243,804 B1	06/05/2001	Cheng	712/228	
	US	6,256,775 B1	07/03/2001	Flynn	717/4	
	US	6,286,132 B1	09/04/2001	Tanaka et al.	717/4	
	US	2001/0032305A1	10/18/2001	Barry	712/34	
	US	6,324,689 B1	11/27/2001	Lowney et al.	717/9	

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LT	US	6,330,662 B1	12/11/2001	Patel et al.	712/236	
	US	2001/0019976A1	02/14/2002	Patel et al.	717/137	
	US	6,351,844 B1	02/26/2002	Bala	717/4	
	US	6,374,364 B1	04/16/2002	McElroy et al.	714/10	
	US	6,430,741 B1	08/06/2002	Mattson, Jr. et al.	717/154	
	US	6,442,585 B1	08/27/2002	Dean et al.	709/108	
	US	6,446,029 B1	09/03/2002	Davidson et al.	702/186	
	US	2002/0124237A1	09/05/2002	Sprunt et al.	717/127	
	US	2002/0129309A1	09/12/2002	Floyd et al.	714/724	
	US	2002/0147965A1	10/10/2002	Swaine et al.	717/124	

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LT	US	6,480,938 B2	11/12/2002	Vondran, Jr.	711/125	
	US	2002/0199179A1	12/26/2002	Lavery et al.	717/158	
	US	6,574,727 B1	06/03/2003	Davidson et al.	712/227	

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